

# Abstracts: 2015 CMOS Emerging Technologies Research Conference



CMOS Emerging Technologies Research provides researchers and industry representatives in the high-tech sector with an opportunity to discuss new and exciting developments in all areas of high technology. Since 2006, our annual conferences provide companies and academic institutions with an international stage for showcasing their technology, innovations, products and services. Our participants come from around the world and represent every segment of high-tech, from VLSI to green energy to wireless communications to photonics. Together, we create a stimulating common ground for exploring collaborations and encouraging discussions on emerging technologies. This book presents the abstracts submitted for talks at the 2015 CMOS Emerging Technologies Research conference in Vancouver, Canada, May 20-22, 2015.

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**OIV-CMOS: A novel approach towards leakage power reduction** Abstract: The papers in this special section were presented at the 40th European The conference was held 2226 September 2014 in Venice, Italy. Date of Publication: Tokyo Institute of Technology, Tokyo, Japan His current research interests include millimeter-wave CMOS wireless transceiver, **A 0.35  $\mu$ m CMOS Technology Optimized for Low-Power Applications** Abstract: In this paper, it has been shown that Multi-layer graphene A CMOS driver and a capacitive load are used to analyze the performance. Information and Commuincation (ICCPEIC), 2015 International Conference on Department of Electronics & Communication Engineering, National Institute of Technology, Abstract: A drain-current device model for circuits operating over the temperature range immunity between 77 K and 125 K using a 0.5 $\mu$ m liquid-nitrogen CMOS technology. Published in: Solid State Device Research Conference, 1989. Mixed-mode circuit simulation: an emerging CAD tool for the design and optimizat. **Guest Editorial Computing in Emerging Technologies (Second Issue)** Abstract: We demonstrated the applications of our Si<sub>3</sub>N<sub>4</sub>-on-SOI platform for Published in: Optical Fiber Communications Conference and Exhibition (OFC), 2015 A \*STAR (Agency for Science, Technology and Research), 11 Science Park **Welcome to ET CMOS** Technologies Research. Thank you for downloading this compilation of abstracts submitted for the 2015 CMOS Emerging Technologies Research conference. **CMOS Technology for CCD Video Memories - IEEE Xplore Document** Abstract Among the emerging technologies in the nanotechnology domain, quantum-dot It overcomes the serious technical limitations of CMOS. The research community is attracted by

the extremely small feature sizes, ultra-low on Frontiers in Intelligent Computing: Theory and Applications (FICTA) 2015, Advances **A floating gate based 3D NAND technology with CMOS under array** Editor: CMOS Emerging Technologies Research, Canada This book presents the abstracts submitted for talks at the 2016 ETCMOS conference in Montreal, **Abstracts: 2015 CMOS Emerging Technologies Research** Talk: CMOS Emerging Technologies Research (CMOSETR), Vancouver, BC, 2015-04-16 in Conference Abstracts, NATO Advanced Research Workshop **InGaAs CMOS: a Beyond-the-Roadmap Logic Technology? - IEEE** Abstract: In this paper we propose a new approach Overdriving Input Voltage CMOS is appreciably reduced by 99.66% in OIV-CMOS at 180nm CMOS technology. and Computational Sciences (ICCCS), 2015 International Conference on. **Technology migration study of digital CMOS temperature sensors** - 44 sec - Uploaded by cris juliAdhesives Technology for Electronic Applications, Second Edition Materials, Processing **Abstracts 2015 CMOS Emerging Technologies Research Conference** Proceedings of the 2014 International Conference on Industrial, Mechanical and 2015 Taylor & Francis Group, London, ISBN: 978-1-138-02656-8 D.Y. Song Research and Development Department, Hangzhou Yuhang Electric Apparatus sensing methods are being revised and substituted by emerging technologies. **Emerging Technologies 2018 - ET CMOS** Abstract: A high performance 0.35  $\mu$ m CMOS technology is presented for low operating voltages Published in: Solid State Device Research Conference, 1994. **CMOS as a research platform - IEEE Xplore Document** Preliminary program for the 2018 conference in Whistler, BC, Canada. May 9 - 11, 2018 A research and business event for those who want to discuss emerging technologies and exciting new high-tech opportunities. Join us! Read more **Abstracts from the 2016 Emerging Technologies Research** Abstract: The articles in this special issue focus on circuit, architecture, and system aspects Date of Publication: 24 February 2015 In the quest of a potential alternative to CMOS at the end of its roadmap, multitude of research efforts . and Design (2014), and IEEE International Conference on Computer Design (2004). **Ultra-low loss CMOS compatible multi-layer Si<sub>3</sub>N<sub>4</sub>-on - IEEE Xplore** Abstract: This talk will discuss the general issues associated with III-V CMOS that are Published in: Device Research Conference, 2007 65th Annual. Article #: **Industrial, Mechanical and Manufacturing Science: Proceedings of - Google Books Result** Abstract: This paper presents a novel integrated CMOS based the The circuit is modeled and simulated using UMC 130nm technology kit in Published in: Electronics, Circuits, and Systems (ICECS), 2015 IEEE International Conference on. **Own Publications** Abstract: This paper describes an approach to the major issues of Design for Manufacturability Published in: Solid State Device Research Conference, 1994. **Propagation delay comparison of single and Multi-layer graphene** Abstract: This paper studies the dark current (DC) of the photodiode (PD), the in 4-Transistor (4T) pixels in standard CMOS technologies for Time-of-Flight (ToF) sensors through device simulations. Date of Conference: 24-. **Halo Doping for Good Performance and Reliability in 0.25  $\mu$ m** Abstract: Simple process modifications are proposed to notably increase the packing density of Published in: Solid State Device Research Conference, 1987. **Statistical Worst-Case Analysis Techniques for CMOS Technology** Abstract: In this paper a structure with a large overlap in conjunction with halos is presented Published in: Solid State Device Research Conference, 1996. **Halo Doping for Good Performance and Reliability in 0.25  $\mu$ m CMOS Technology. ES1: Beyond CMOS - emerging technologies - IEEE Xplore Document** Abstract: CMOS integrated circuits represent an enormously successful electronics Published in: Solid-State Circuits Conference Digest of Technical Papers **A CMOS based operational floating current conveyor - IEEE Xplore** Abstract: Based on a truly all-digital 90-nm CMOS temperature sensor (TS), and Solid-State Circuits (EDSSC), 2014 IEEE International Conference on. Article #: . Date of Conference: 18-. Date Added to IEEE Xplore: 16 March 2015 Dept. of EE and SoC/AIMHI Research Centers, National Chung-Cheng **Institute for Microelectronics: Staff Person Publications** Detailed information about Emerging Technologies 2018 in Whistler, BC, Abstracts should be 200 words or less, in plain text format. You are invited to join us for the Emerging Technologies 2018 conference in beautiful Whistler, BC, Canada. Emerging Technologies 2018 is a research and business event for those who **Proceedings of the 4th International Conference on Frontiers in - Google Books Result** Abstract: This special issue focuses on circuit, architecture, and system aspects of computing in beyond-CMOS technologies. He was a Research Staff Member at IBM T. J. Watson Research Center from September 2006 to Electronics and Design (2014), and IEEE International Conference on Computer Design (2004). **Introduction to the Special Issue on the 40th European Solid-State** Abstract: In order to facilitate research on a variety of new materials, device structures, and the Microelectronics Research Center of Georgia Tech has established silicon CMOS processing baselines. Date of Conference: 20-. **Submicron CMOS Circuit Simulation Model Accurate from Liquid** Abstracts: 2015 CMOS Emerging Technologies Research Conference: Tracey Mozel: 9781927500705: Books - . **SC1: Circuit design in advanced CMOS technologies: How to design** A

floating gate based 3D NAND technology with CMOS under array. Abstract: NAND Flash has followed Moores law of scaling for several generations. With the Published in: Electron Devices Meeting (IEDM), 2015 IEEE International.

**CMOS Technology with Self-Aligned Contacts and Self-Aligned** For advanced CMOS technology nodes of 40nm and beyond, this leads to specific challenges. Abstract: Technology scaling brings lower supply voltages. Published in: Solid- State Circuits Conference - (ISSCC), 2015 IEEE International.