

Xilinx Student Edition, Version 1.5



This software package provides a complete, ready-to-use, inexpensive digital logic learning environment which enables fast design and prototyping using industry standard-design tools from the #1 programmable logic vendor. The package includes software tools, lab design files, step-by-step, hands-on tutorials, and a coupon for discount on optional purchase of hardware development boards. Stressing actual construction and testing of logic circuitry, it presents a complete set of lab sessions on using FPGAs (Field Prog. Gate Arrays) and CPLDs (Complex Prog. Logic Devices) for beginning or intermediate logic design.

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